

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

**1-8 (cancelled).**

**9 (currently amended).** A liquid crystal display device comprising:

- (a) a pixel electrode, wherein said pixel electrode includes a pixel edge section ~~having been exposed to light, at a periphery thereof,~~
- (b) a thin film transistor located adjacent to said pixel electrode and at one side of said pixel electrode;
- (c) a first signal line extending along said one side of said pixel electrode; and
- (d) a second signal line extending along the other side of said pixel electrode,  
said pixel electrode being formed with a cut-out portion at the other side thereof, said cut-out portion having a length equal to a length along which said first signal line cannot be adjacent to said pixel electrode by said thin film transistor,  
a first length along which said pixel electrode and said first signal line are adjacent to each other being equal to a second length along which said pixel electrode and said second signal line are adjacent to each other,  
a first space between said pixel edge section and said first signal line being equal to a second space between said pixel edge section and said second signal line.

**10 (cancelled).**

**11 (previously presented).** The liquid crystal display device as set forth in claim 9, wherein said pixel electrode is formed on a layer in which said first and second signal lines are also formed.

**12 (previously presented).** The liquid crystal display device as set forth in claim 9, wherein said first and second signal lines are formed on a first layer, and said pixel electrode is formed on a second layer which is electrically isolated from said first layer by an insulating layer sandwiched between said first and second layers.

**13 (currently amended).** A liquid crystal display device comprising:

(a) a plurality of pixel electrodes each spaced away from adjacent ones by a predetermined distance, wherein said pixel electrodes include pixel edge sections ~~having been exposed to light, at a periphery thereof;~~

(b) first signal lines each extending along one side of each of said pixel electrodes, each of said first signal lines having a bending portion which extends along a periphery of each of said pixel electrodes; and

(c) second signal lines each extending along the other side of each of said pixel electrodes, each of said second signal lines having a bending portion which extends along a periphery of each of said pixel electrodes,

at least one of said first and second signal lines having a projecting portion extending towards said pixel electrodes,

a first length along which said pixel electrodes and said first signal lines are adjacent to each other being equal to a second length along which said pixel electrodes and said second signal lines are adjacent to each other,

a first space between said pixel edge sections and said first signal lines being equal to a second space between said pixel edge sections and said second signal lines.

**14 (cancelled).**

**15 (previously presented).** The liquid crystal display device as set forth in claim 13, wherein each of said pixel electrodes is formed on a layer in which said first and second signal lines are also formed.

**16 (previously presented).** The liquid crystal display device as set forth in claim 13, wherein said first and second signal lines are formed on a first layer, and said pixel electrodes are formed on a second layer which is electrically isolated from said first layer by an insulating layer sandwiched between said first and second layers.

**17 (currently amended).** A method of fabricating a liquid crystal display device including a pixel electrode, a pixel edge section, a first signal line extending along one side of said pixel electrode, and a second signal line extending along the other side of said pixel electrode, comprising the steps of:

- (a) forming a scanning line on a transparent substrate, and then, forming a gate insulating film on said scanning line and said transparent substrate;
- (b) forming a channel on said gate insulating film above said scanning line;
- (c) forming said pixel edge section and said first and second signal lines so that a first length along which said pixel electrode and said first signal line are adjacent to each other is equal to a second length along which said pixel electrode and said second signal line are adjacent to each other, and a first space between said pixel edge section and said first signal line is equal to a second space between said pixel edge section and said second signal line,
- (d) forming said pixel electrode ~~and said pixel edge section~~; and
- (e) covering a product resulting from said step (d) with an insulating film layer.

**18 (previously presented).** The method as set forth in claim 17, wherein said pixel electrode is formed between said first and second signal lines on a common layer in said step (d).

**19 (currently amended).** The method as set forth in claim 18, wherein said pixel electrode is formed, after said gate insulating film has been formed, on said gate insulating film above a region sandwiched between said first and second signal lines.

**20 (currently amended).** A method of fabricating a liquid crystal display device including a pixel electrode, a pixel edge section, a first signal line extending along one side of said pixel electrode, and a second signal line extending along the other side of said pixel electrode, comprising the steps of:

- (a) forming a scanning line on a transparent substrate, and then, forming a gate insulating film on said scanning line and said transparent substrate;
- (b) forming a channel on said gate insulating film above said scanning line;
- (c) forming said pixel edge section and said first and second signal lines so that at least one of said first and second signal lines has a projecting portion extending towards said pixel electrode and that a first length along which said pixel electrode and said first signal line are adjacent to each other is equal to a second length along which said pixel electrode and said second signal line are adjacent to each other, and a first space between said pixel edge section and said first signal line is equal to a second space between said pixel edge section and said second signal line,
- (d) forming said pixel electrode and said pixel edge section; and
- (e) covering a product resulting from said step (d) with an insulating film layer.

**21 (previously presented).** The method as set forth in claim 20, wherein said pixel electrode is formed between said first and second signal lines on a common layer in said step (d).

**22 (currently amended).** The method as set forth in claim 20, wherein said pixel electrode is formed, after said gate insulating film has been formed, on said gate insulating film above a region sandwiched between said first and second signal lines.

**23 (currently amended).** A method of fabricating a liquid crystal display device including a pixel electrode, a pixel edge section, a first signal line extending along one side of said pixel electrode, and a second signal line extending along the other side of said pixel electrode, comprising the steps of:

(a) forming a scanning line on a transparent substrate, and then, forming a gate insulating film on said scanning line and said transparent substrate;

(b) forming a channel on said gate insulating film above said scanning line;

(c) forming said pixel edge section with a cut-out portion at the other side of said pixel electrode and forming said first and second signal lines so that said cut-out portion has a length equal to a length along which said first and/or second signal line(s) cannot be adjacent to said pixel electrode by a thin film transistor formed at one side of said pixel electrode,

a first length along which said pixel electrode and said first signal line are adjacent to each other being equal to a second length along which said pixel electrode and said second signal line are adjacent to each other,

a first space between said pixel electrode and said first signal line being equal to a second space between said pixel electrode and said second signal line,

(d) forming said pixel electrode ~~and said pixel edge section~~; and

(e) covering a product resulting from said step (d) with an insulating film layer.

**24 (previously presented).** The method as set forth in claim 23, wherein said pixel electrode is formed between said first and second signal lines on a common layer in said step (d).

**25 (currently amended).** The method as set forth in claim 23, wherein said pixel electrode is formed, after said gate insulating film has been formed, on said gate insulating film above a region sandwiched between said first and second signal lines.

**26 (currently amended).** A method of fabricating a liquid crystal display device including a plurality of pixel electrodes each spaced away from adjacent ones by a predetermined distance, said pixel electrodes having pixel edge sections, first signal lines each extending along one side of each of said pixel electrodes, each of said first signal lines having a projecting bending portion which extends along a periphery of each of said pixel electrodes, and second signal lines each extending along the other side of each of said pixel electrodes, each of said second signal lines having a projecting bending portion which extends along a periphery of each of said pixel electrodes, comprising the steps of:

- (a) forming a scanning line on a transparent substrate, and then, forming a gate insulating film on said scanning line and said transparent substrate;
- (b) forming a channel on said gate insulating film above said scanning line;
- (c) forming said pixel edge sections and said first and second signal lines so that at least one of each of said first signal lines and each of said second signal lines has a projecting portion extending towards each of said pixel electrodes and that a first length along which each of said pixel electrodes and each of said first signal lines are adjacent to each other is equal to a second length along which each of said pixel electrodes and each of said second signal lines are adjacent to each other, and a first space between each of said pixel edge sections and each of said first signal lines is equal to a second space between each of said pixel edge sections and each of said second signal lines,
- (d) forming said pixel electrodes ~~and said pixel edge sections~~; and

(e) covering a product resulting from said step (d) with an insulating film layer.

**27 (previously presented).** The method as set forth in claim 26, wherein each of said pixel electrodes is formed between each of said first signal lines and each of said second signal lines on a common layer in said step (d).

**28 (currently amended).** The method as set forth in claim 26, wherein each of said pixel electrodes is formed, after said gate insulating film has been formed, on said gate insulating film above a region sandwiched between said first and second signal lines.